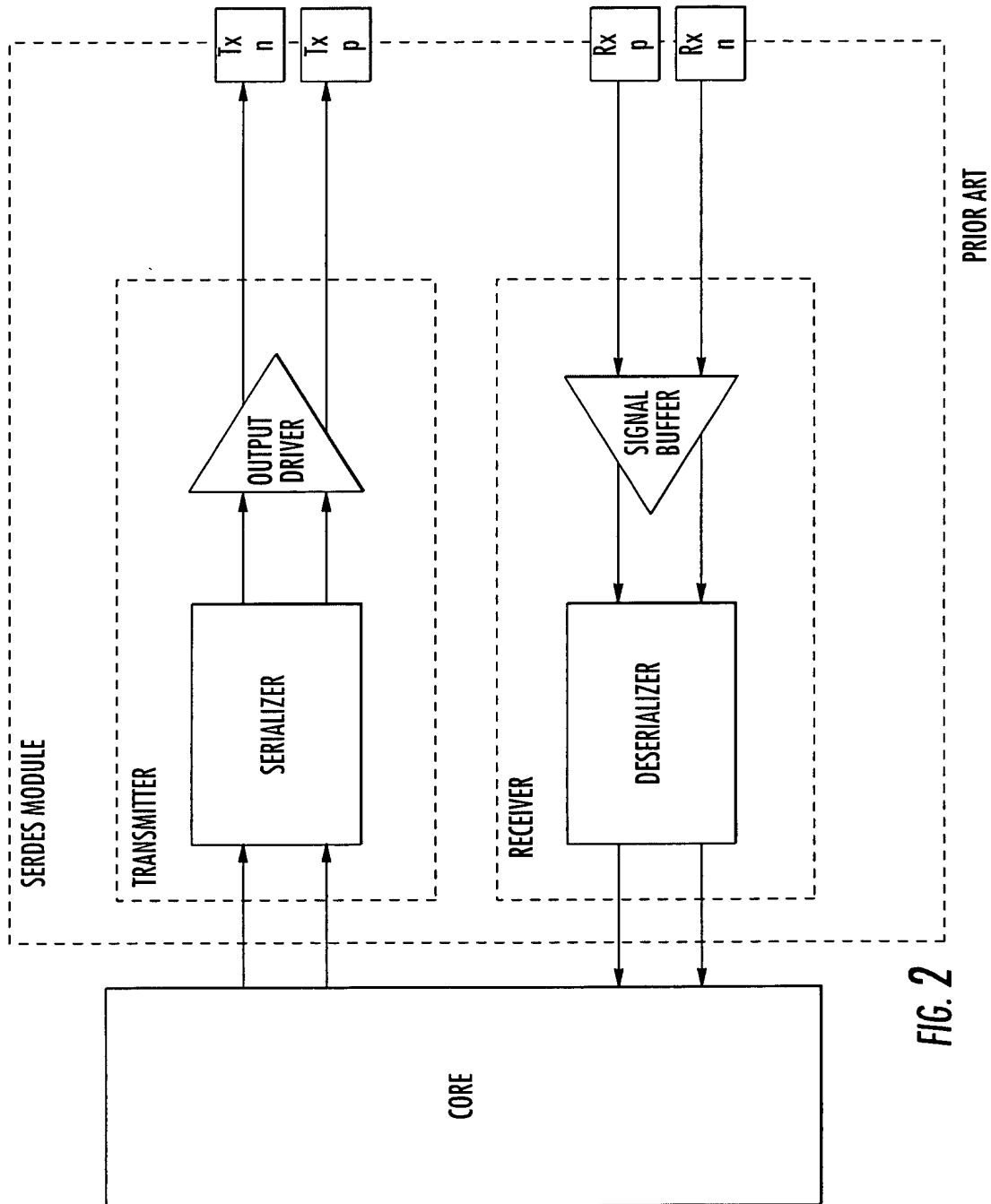


FIG. 1

PRIOR ART

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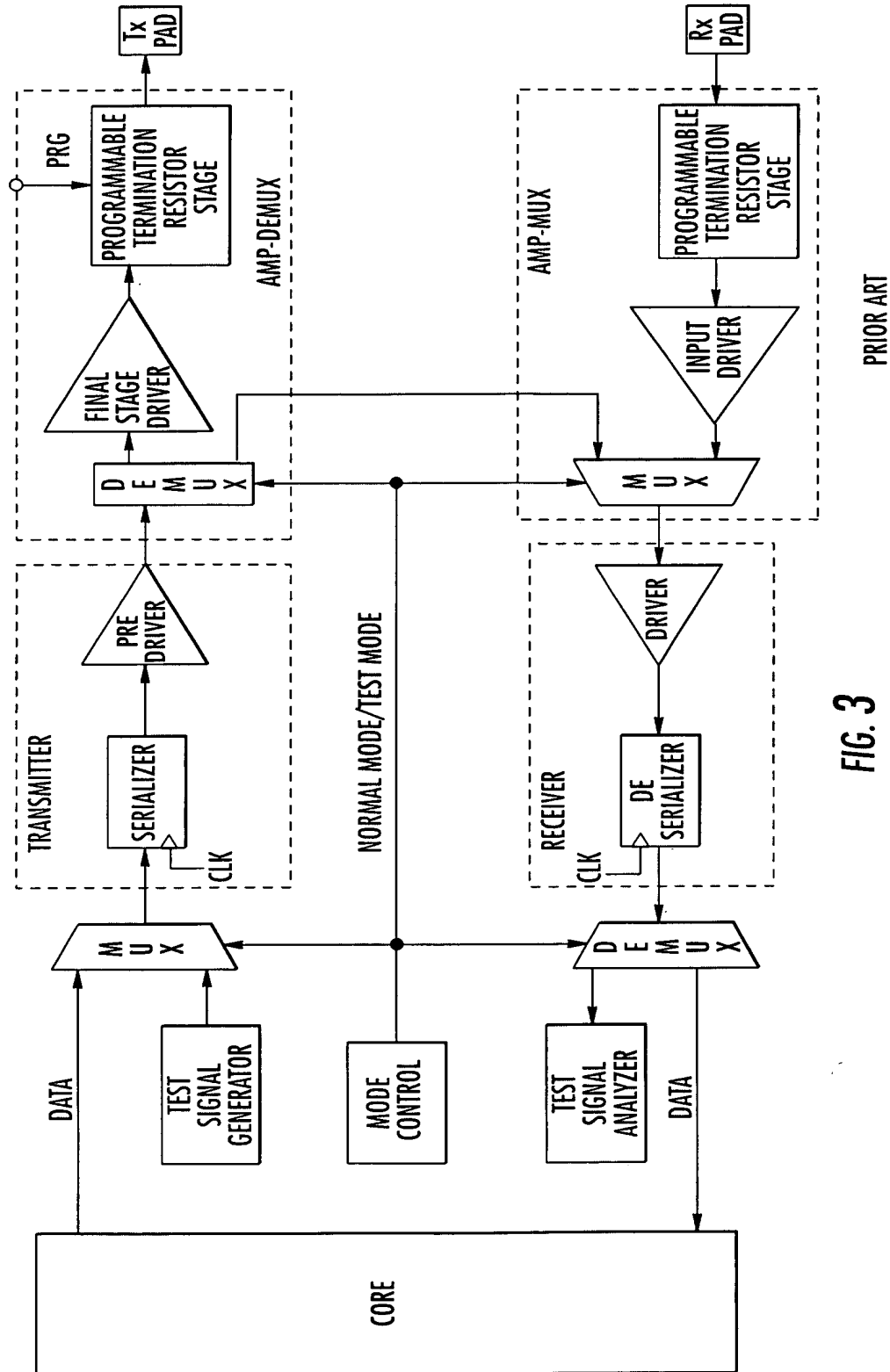


FIG. 3

# REPLACEMENT SHEET

Title: TEST SWITCHING CIRCUIT FOR A HIGH SPEED DATA INTERFACE

Applicant: Argüelles et al.

Serial No. 10/788,545

Docket No.: 1406/186

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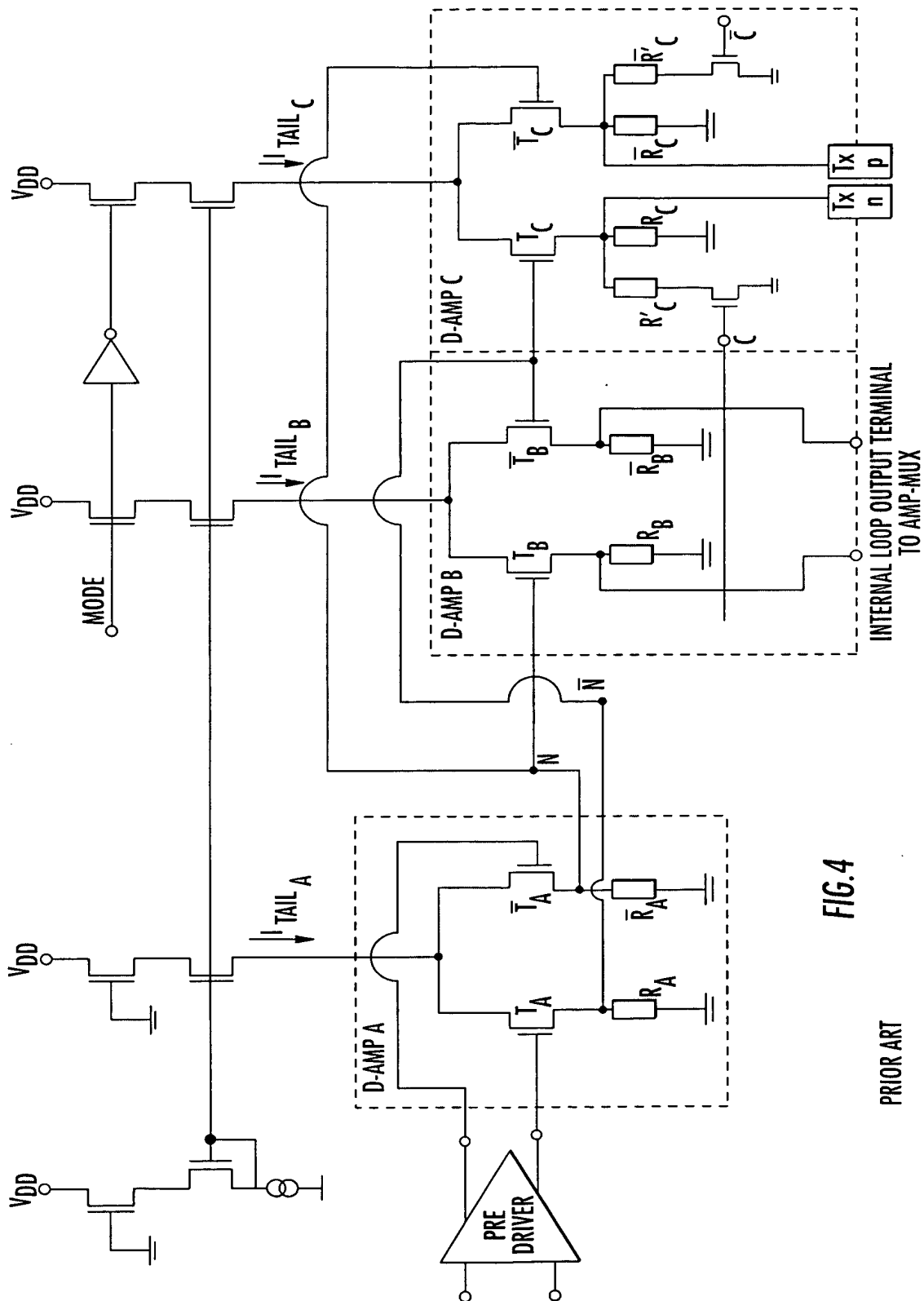


FIG. 4

PRIOR ART

# REPLACEMENT SHEET

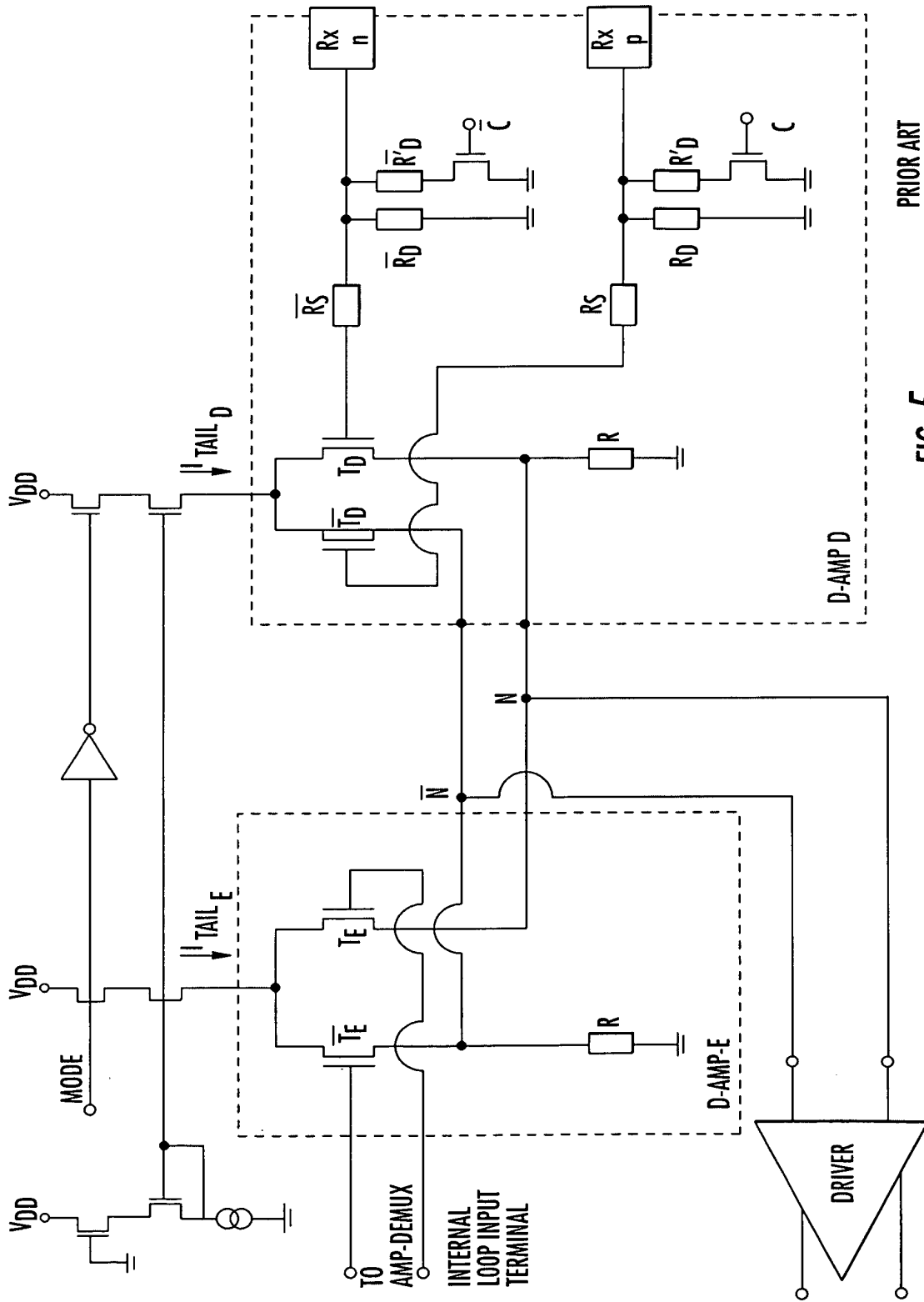
Title: TEST SWITCHING CIRCUIT FOR A HIGH SPEED DATA INTERFACE

Applicant: Argüelles et al.

Serial No. 10/788,545

Docket No.: 1406/186

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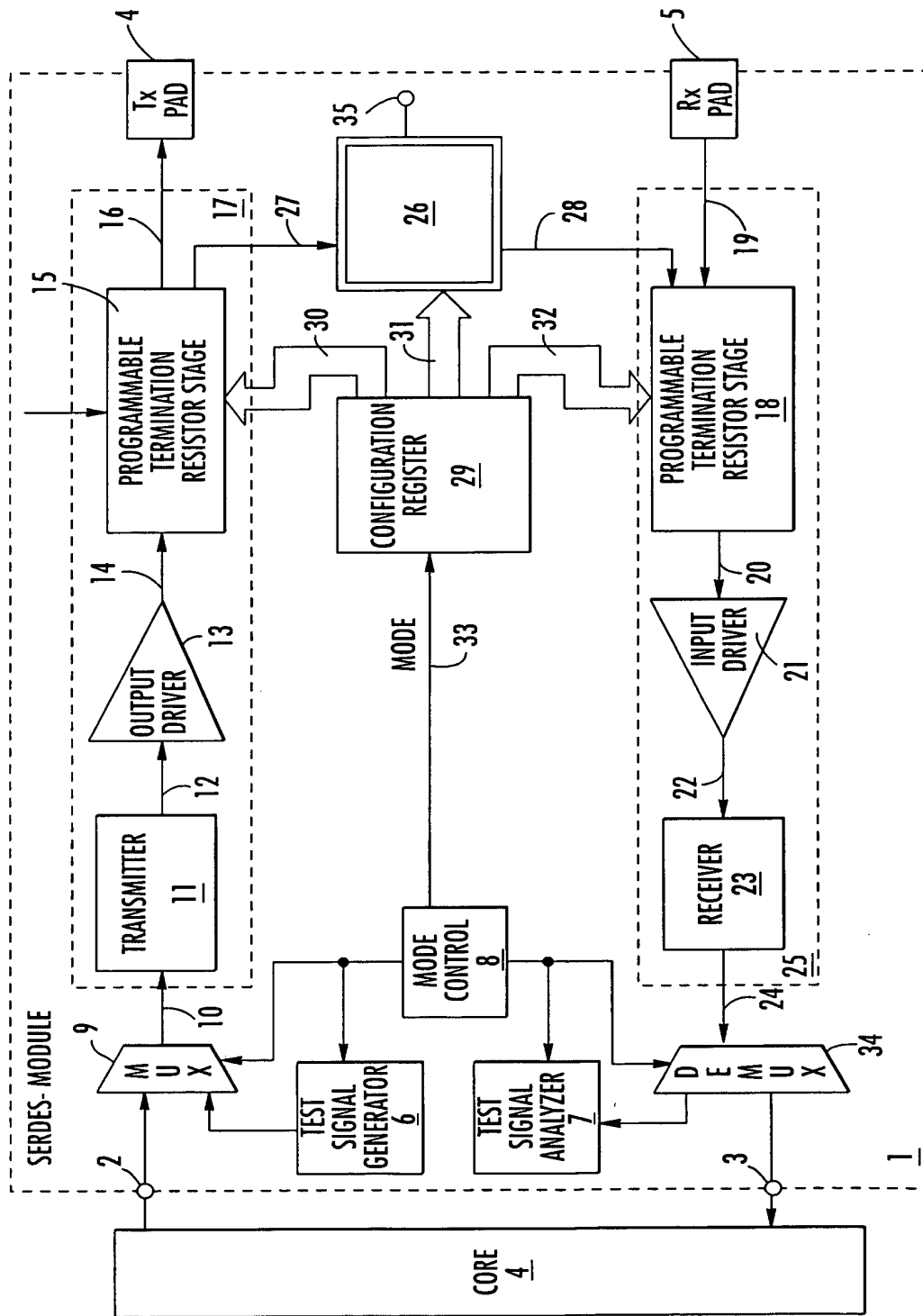
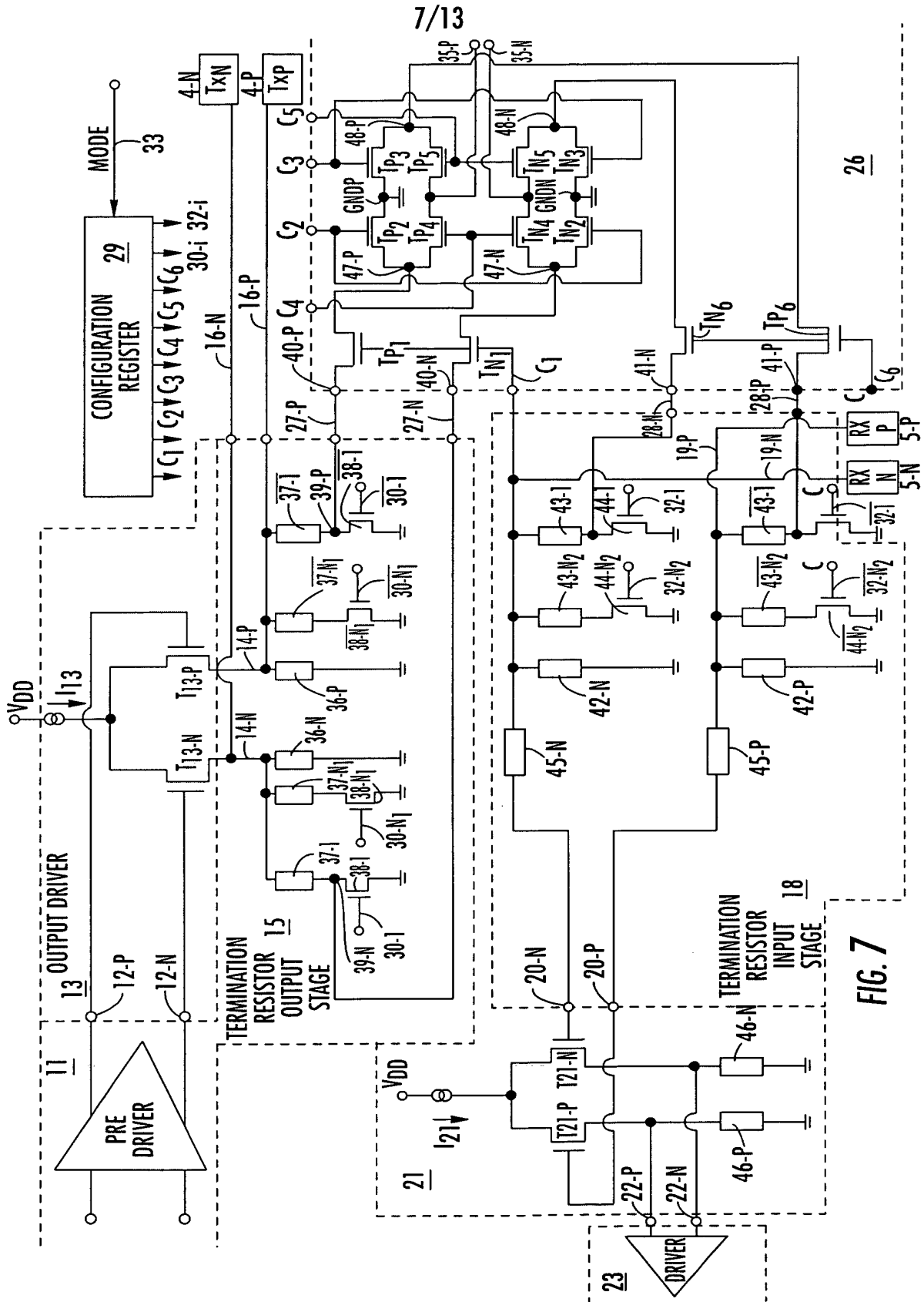


FIG. 6



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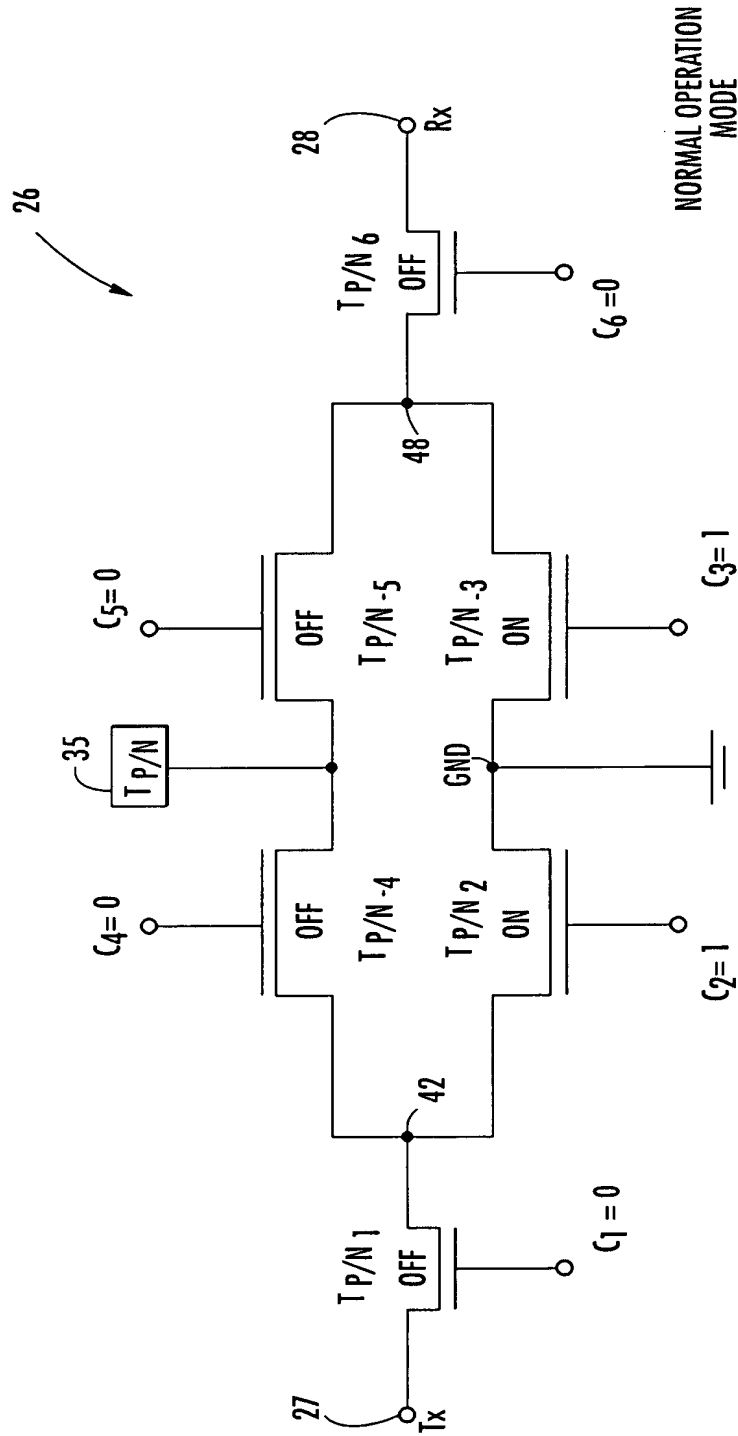
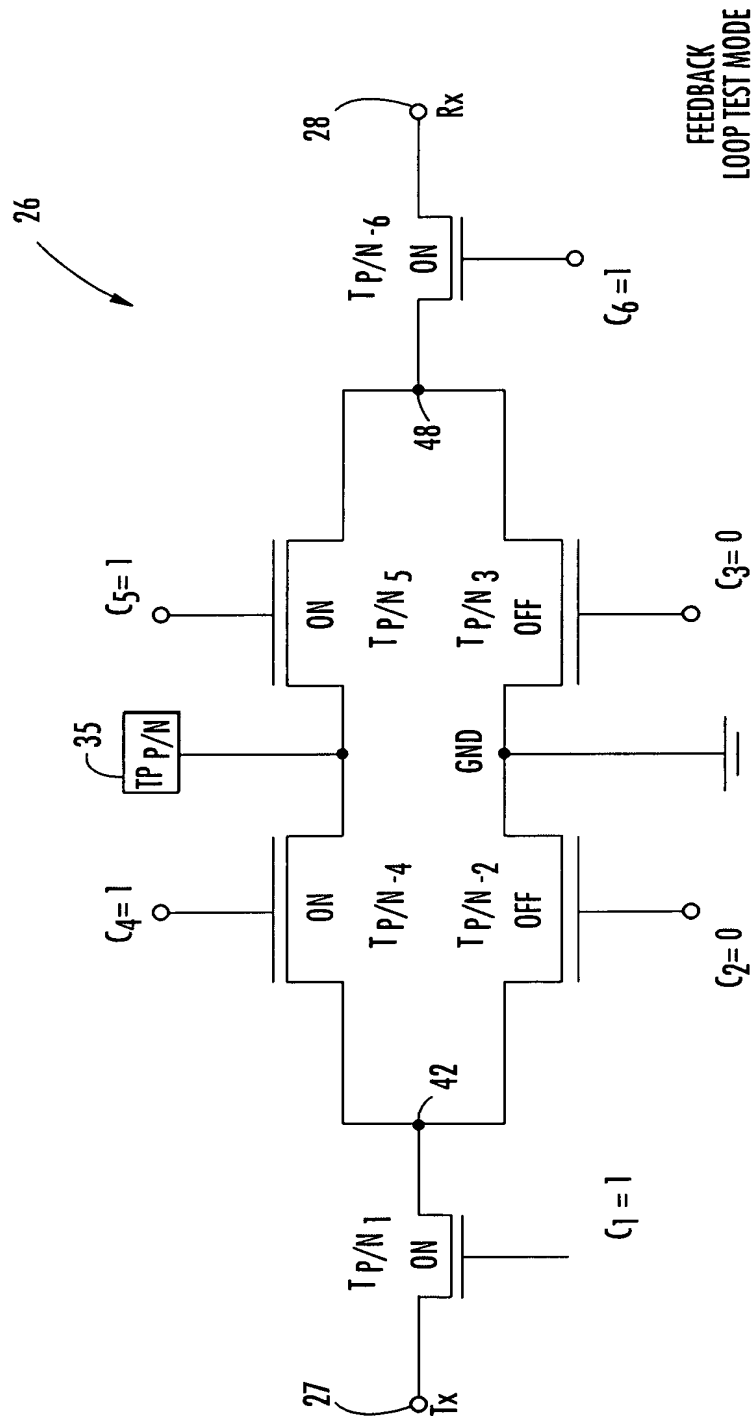


FIG. 8





**FIG. 9**

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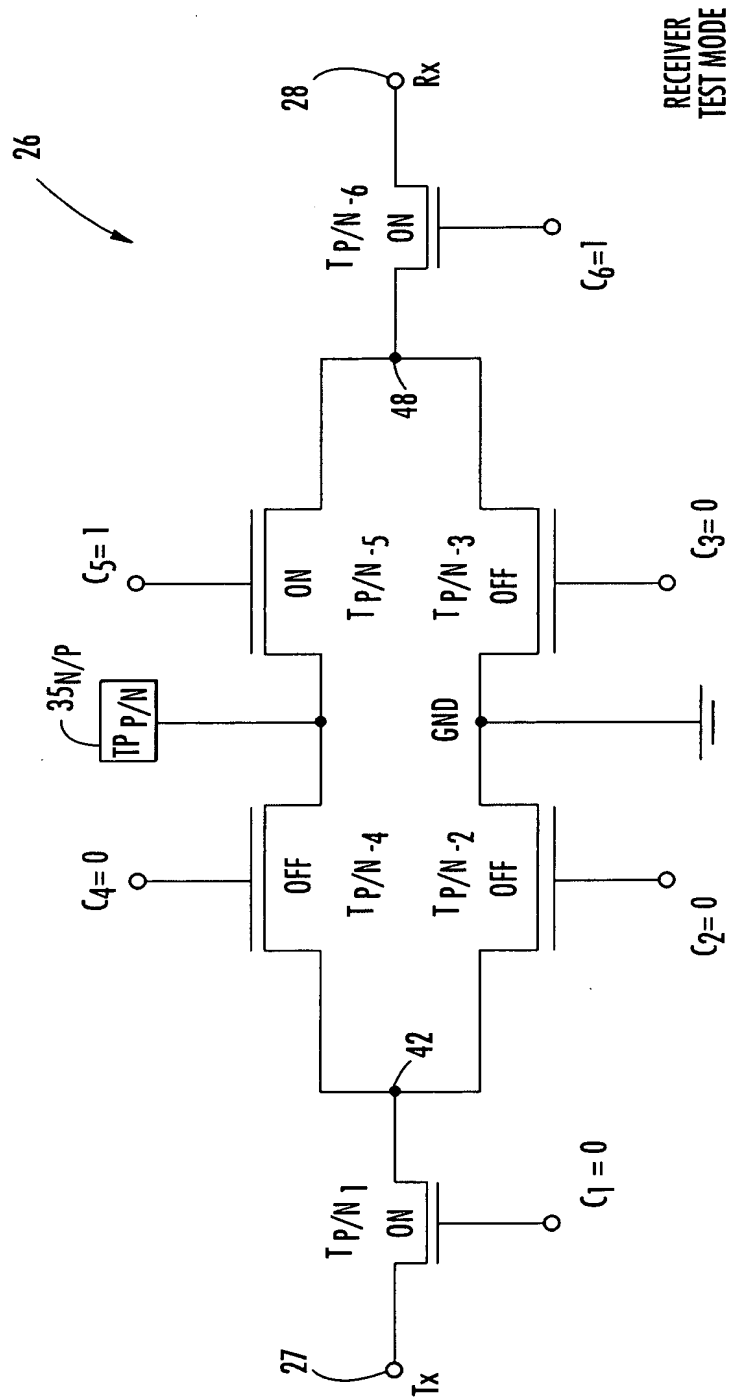


FIG. 10

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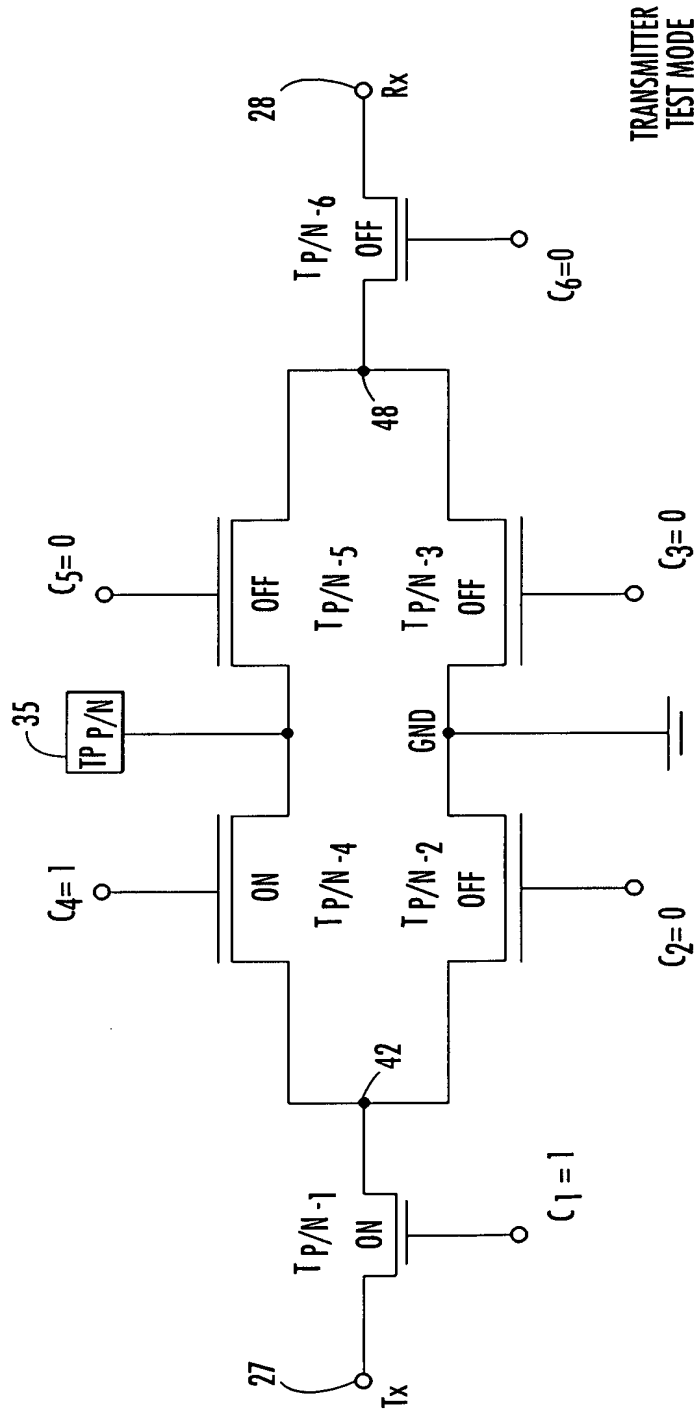


FIG. 11

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CONFIGURATION REGISTER

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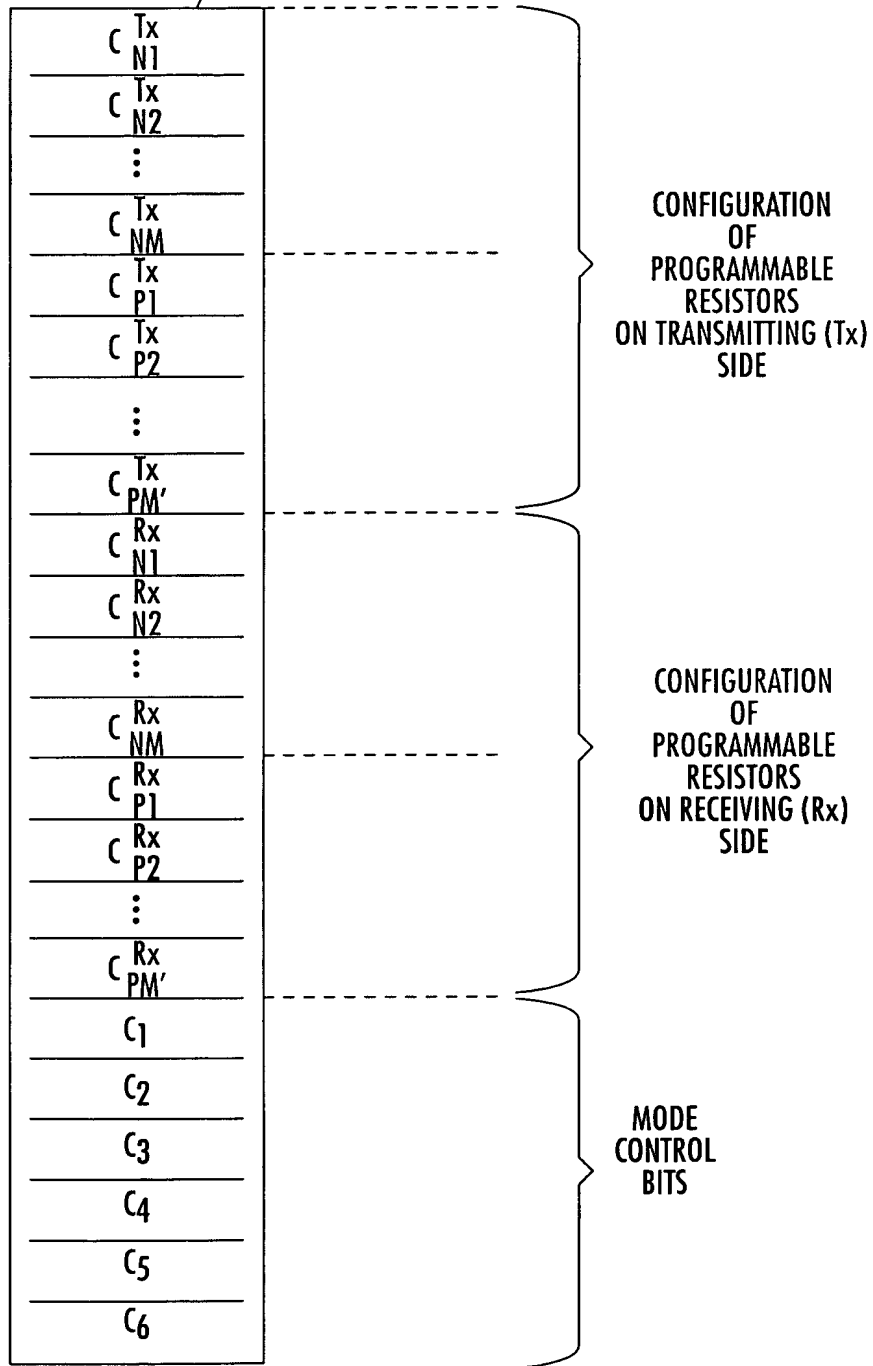


FIG. 12

**REPLACEMENT SHEET**

Title: TEST SWITCHING CIRCUIT FOR A HIGH  
SPEED DATA INTERFACE

Applicant: Argüelles et al.

Serial No. 10/788,545

Docket No.: 1406/186

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CONTROL BITS	NORMAL OPERATION MODE	FEEDBACK TEST MODE	RECEIVER TEST MODE	TRANSMITTER TEST MODE
C <sub>1</sub>	0	1	0	1
C <sub>2</sub>	1	0	0	0
C <sub>3</sub>	1	0	0	0
C <sub>4</sub>	0	1	0	1
C <sub>5</sub>	0	1	1	0
C <sub>6</sub>	0	1	1	0

**FIG. 13**